

**Notice of References Cited**

Application/Control No.

09/752,134

Applicant(s)/Patent Under  
Reexamination  
NEIGER ET AL.

Examiner

Kevin Schubert

Art Unit

2137

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,795,966	09-2004	Lim et al.	718/1
	B	US-5,386,552	01-1995	Garney, John I.	714/10
	C	US-5,511,217	04-1996	Nakajima et al.	718/100
	D	US-6,496,847	12-2002	Bugnion et al.	718/1
	E	US-6,795,966	09-2004	Lim et al.	718/1
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rozycki, Maciej W. Protected Mode Virtual Interrupts (PVI) on Pentium and SL-enhanced i486 Intel processors. 1996. Retrieved from the Internet: <URL:http://www.rcollins.org/articles/pvi1/pvi1.html>.
	V	Walker, Wade and Cragon, Harvey. Interrupt Processing in Concurrent Processors. June 1995. Computer. pp. 36-46.
	W	Collins, Robert R. Details of Intel's Virtual Mode Extensions (VME). Retrieved from the Internet: <URL:http://www.rcollins.org/ddj/Mar98/Mar98.html>.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.